



UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: SHIBATA
Serial No.: 10/756,961
Filed: January 13, 2004
Confirmation No.: 4810
Due Date: February 14, 2007
Title: SEMICONDUCTOR MEMORY DEVICE WITH TRENCH-TYPE
STACKED CELL CAPACITORS AND METHOD FOR
MANUFACTURING THE SAME

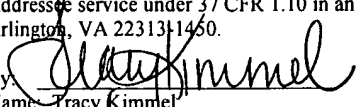
Examiner: PHAM
Group Art: 2814
Docket: 10873.0871USD1
Notice of Allow Date: November 14, 2006

CERTIFICATE UNDER 37 CFR 1.10

Express Mail mailing label number: EV 802672451 US

Date of Deposit: January 05, 2007

I hereby certify that the papers listed below are being deposited with the United States Postal Service Express Mail Post Office to Address service under 37 CFR 1.10 in an envelope addressed to: Mail Stop ISSUE FEE, Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450.

By: 
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Commissioner for Patents
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PATENT TRADEMARK OFFICE

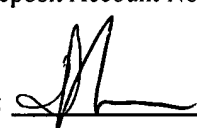
Commissioner:

The following papers are transmitted herewith:

- ☒ Transmittal Sheet in duplicate containing Certificate of Mailing
- ☒ Part B – Fees Transmittal
- ☒ Amendment Under 37 CFR 1.312
- ☒ Checks in the amount of \$1400 for Issue Fee and \$300 for Publication Fee
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